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[Parametric Real-time Reasoning - Alur, Henzinger, Vardi \(1993\)](#) (Correct) (46 citations)

Parametric **Real-time** Reasoning Rajeev Alur AT&T Bell

Parametric **Real-time** Reasoning Rajeev Alur AT&T Bell Laboratories

www.cis.upenn.edu/~alur/Stoc93.ps.gz

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[Handoffs in Cellular Wireless Networks: The Daedalus.. - Srinivasan Seshan \(1996\)](#) (Correct) (31 citations)

**times.** Unfortunately, many applications, such as **real-time** multimedia applications and reliable in data loss or large variations in packet delivery **times**. Unfortunately, many applications, such as loss. We first describe the limitations of the **standard** IP routing mechanisms in a mobile environment. daedalus.cs.berkeley.edu/publications/kluwer.ps.gz

[Computing Accumulated Delays in Real-time Systems - Alur, Courcoubetis, Henzinger \(1993\)](#) (Correct) (19 citations)

Computing Accumulated Delays in **Real-time** Systems Rajeev Alur y Costas

Computing Accumulated Delays in **Real-time** Systems Rajeev Alur y Costas Courcoubetis z

www.cis.upenn.edu/~alur/Cav93.ps.gz

[A Calculus of Durations on Abstract Domains: Completeness and.. - Guelev \(1998\)](#) (Correct) (6 citations)

as a generalization of the result on the **standard real-time** frame coped with in [HZ92]The choice of a generalization of the result on the **standard real-time** frame coped with in [HZ92]The choice of abstract frames, as a generalization of the result on the **standard real-time** frame coped with in [HZ92]The ftp.iist.unu.edu/pub/techreports/report139.ps.gz

[Back to the Future: Towards a Theory of Timed Regular Languages - Alur, Henzinger \(1992\)](#) (Correct) (19 citations)

-words in which every symbol is labeled with a **real-valued time**. These automata were designed to lead

Back to the Future: Towards a Theory of **Timed** Regular Languages Rajeev Alur Thomas A.

www.cis.upenn.edu/~alur/Focs92.ps.gz

[Mechanical Verification of a Generalized Protocol for Byzantine.. - Shankar \(1992\)](#) (Correct) (16 citations)

from J. Vytopil, editor: Formal Techniques in **Real-Time** and FaultTolerant Systems, Nijmegen, The ftp.csl.sri.com/reports/postscript/fttrft92-ns.ps.gz

[Methods for Encrypting and Decrypting MPEG Video Data Efficiently - Tang \(1996\)](#) (Correct) (8 citations)

commerce on the Internet such as video-ondemand and **real-time** video multicast. However, traditional on the Internet such as video-ondemand and **real-time** video multicast. However, traditional are based on the widely used JPEG and MPEG **standards**. We also conduct a series of experimental www.ini.cmu.edu/~sirbu/lt00/paper/mm96.ps.gz

[Towards Interprocess Communication and Interface Synthesis .. - Fischer, Muth, Färber \(1998\)](#) (Correct) (2 citations)

and Interface Synthesis for a Heteogenous **Real-Time** Rapid Prototyping Environment Franz

and Interface Synthesis for a Heteogenous **Real-Time** Rapid Prototyping Environment Franz Fischer

of the specification language SDL, links the **standard** components of our multiprocessor system in an

ftp.lpr.e-technik.tu-muenchen.de/pub/papers/rtsq/codes98.ps.gz

[CAISARTS: A Tool for Real-Time Scheduling Assistance - Humphrey, Stankovic \(1996\)](#) (Correct) (3 citations)

CAISARTS: A Tool for **Real-Time** Scheduling Assistance 1 Marty Humphrey and

CAISARTS: A Tool for **Real-Time** Scheduling Assistance 1 Marty Humphrey and John

<ftp.cs.umass.edu/pub/techrept/techreport/1996/UM-CS-1996-007.ps>

Measuring System Performance with Reprogrammable Hardware - Shand (1992) (Correct) (3 citations)

By contrast, in this application we rely on the **real-time**, concurrent aspects of a PAM that is contrast, in this application we rely on the **real-time**, concurrent aspects of a PAM that is relatively occur in far less **time** than the resolution of **standard real-time** clock devices and involve system  
[pam.devinci.fr/pub/doc/PRL-RR/19.ps.Z](http://pam.devinci.fr/pub/doc/PRL-RR/19.ps.Z)

The GMD-Snake - Real-Time Scheduling of a Flexible Robot.. - Edgar Nett (1997) (Correct) (1 citation)

The GMD-Snake -**Real-Time** Scheduling of a Flexible Robot Application at  
 The GMD-Snake -**Real-Time** Scheduling of a Flexible Robot Application at  
 we are exploiting the dispatching service of a **standard real-time** operating system, currently VxWorks.  
[set.gmd.de/RS/Papers/SnakeSched.ps](http://set.gmd.de/RS/Papers/SnakeSched.ps)

Expressing and Enforcing Timing Constraints in a.. - DiPippo, Ginis.. (1996) (Correct) (1 citation)

02881 russ@nosc.mil lastname@cs.uri.edu Abstract **Real-time** distributed applications such as automated  
[www.infosys.tuwien.ac.at/Research/Corba/archive/special/ri-tr97-252.ps.gz](http://www.infosys.tuwien.ac.at/Research/Corba/archive/special/ri-tr97-252.ps.gz)

An Example of an Interrelated Specification - Two Responses (Correct)

Timing Constraints of **Real-Time** Systems 12 Slide 22 An Example of an  
 Timing Constraints of **Real-Time** Systems 12 Slide 22 An Example of an Interrelated  
 ffl Is each kind of constraint meaningful for **standard real-time** systems with respect to user behavior,  
[kwanwoo.postech.ac.kr/CS623/timing.ps](http://kwanwoo.postech.ac.kr/CS623/timing.ps)

Modeling Micro-Controller Peripherals for.. - Hsieh, Lavagno.. (Correct)

is used to provide the performance required to meet **real-time** constraints. Hardware components can be to provide the performance required to meet **real-time** constraints. Hardware components can be either using the services provided by some customized or **standard Real-Time** Operating System to coordinate the  
[ic.eecs.berkeley.edu/pub/HWSW/publications/CODES97\\_Periph.ps.gz](http://ic.eecs.berkeley.edu/pub/HWSW/publications/CODES97_Periph.ps.gz)

Designing an Embedded Hard Real-Time System: A Case Study - Colnatic, Cheung, Halang (Correct)

Designing an Embedded Hard **Real-Time** System: A Case Study Matjaz Colnatic 1 C.  
 Designing an Embedded Hard **Real-Time** System: A Case Study Matjaz Colnatic 1 C. T.  
 closely interleaved: a compiler for an adapted **standard real-time** programming language, and a program  
[www.comp.polyu.edu.hk/~csronnie/europar.ps](http://www.comp.polyu.edu.hk/~csronnie/europar.ps)

Constraint Oriented Specification for Timed CSP - Bryans, Pearson (Correct)

of parallel systems and (our version of) PTL is a **real-time** temporal logic designed to capture  
 Constraint Oriented Specification for Timed CSP Jeremy Bryans 1 and Justin Pearson 2  
[www.csd.uu.se/~justin/madras.ps](http://www.csd.uu.se/~justin/madras.ps)

Acknowledgements - Would Like (Correct)

.6 2.2 **Real-time** Transport Layer Protocols .  
 always been a continuing source of guidance. In my **time** in the research group, I have met many outstanding  
[manip.crhc.uiuc.edu/pub/papers/PostScript/TM14/TM14.ps.gz](http://manip.crhc.uiuc.edu/pub/papers/PostScript/TM14/TM14.ps.gz)

Improving Processor Utilization with a Task.. - Färber, Fischer.. (1997) (Correct)

Model based Application Specific Hard **Real-Time** Architecture Georg Farber Franz Fischer  
 Model based Application Specific Hard **Real-Time** Architecture Georg Farber Franz Fischer Thomas  
[ftp.lpr.e-technik.tu-muenchen.de/pub/papers/rtsg/rtcsa97.ps.gz](http://ftp.lpr.e-technik.tu-muenchen.de/pub/papers/rtsg/rtcsa97.ps.gz)

Parallel Hardware and Parallel Software: a Reconciliation - Welch (Correct)

hidden from those trying to solve problems in the **real** world. 2 The Pursuit of Understanding The **real**  
 around a particular problem in a tenth of the **time** (or solving a ten-times more ambitious problem in  
 and severe portability problems. Recent **standardisation** efforts (for message-passing interfaces  
[nscsp01.physics.upenn.edu/parallel/groups/selhpc/crisis/presentations/welch/ntug95.ps.gz](http://nscsp01.physics.upenn.edu/parallel/groups/selhpc/crisis/presentations/welch/ntug95.ps.gz)

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[MPEG-2 Transport over ATM Networks - Tryfonas, Varma \(1999\)](#) [\(Correct\)](#) [\(3 citations\)](#)format is used in the Digital Versatile Disk **#DVD# standard** for playback in stand-alone environments, it [www.sprintlabs.com/People/tryfonas/papers/ucsc-crl-99-1.pdf](http://www.sprintlabs.com/People/tryfonas/papers/ucsc-crl-99-1.pdf)[On Runlength-Limited Coding with DC Control - Roth \(2000\)](#) [\(Correct\)](#) [\(1 citation\)](#)compared to the EFMPPlus code used in the **DVD standard**. Keywords: Runlength-limited coding, Optical is the EFMPPlus code which is part of the **DVD standard**. Design considerations are summarized in [www.cs.technion.ac.il/~ronny/PUB/2000/dc.ps](http://www.cs.technion.ac.il/~ronny/PUB/2000/dc.ps)[TriMedia CPU64 Application Domain and . . . - Riemens, Vissers, Schutten.. \(1999\)](#) [\(Correct\)](#)on the design of such systems. For example, the **DVD standard** not only supports the playback of a [www.cs.berkeley.edu/~vissers/iccd99\\_appl.pdf](http://www.cs.berkeley.edu/~vissers/iccd99_appl.pdf)[A Reconfigurable Pipelined IDCT for Low-Energy Video.. - Kim, Ziesler..](#) [\(Correct\)](#)video broadcast (DVB)digital versatile disc (**DVD**)**standard**-definition television (DTV)high-definition [www.eecs.umich.edu/~suhwan/papers/asic00.ps](http://www.eecs.umich.edu/~suhwan/papers/asic00.ps)[How Many Pixels To Watermark? - Chandramouli, Memon \(2000\)](#) [\(Correct\)](#)of embedded data [1]For example, the proposed **DVD standard** will embed only two bits of information. Given [vulcan.ee.iastate.edu/~mouli/waterc.ps](http://vulcan.ee.iastate.edu/~mouli/waterc.ps)Try your query at: [Amazon](#) [Barnes & Noble](#) [Google \(RI\)](#) [Google \(Web\)](#) [CSB](#) [DBLP](#)CiteSeer - [citeseer.org](http://citeseer.org) - [Terms of Service](#) - [Privacy Policy](#) - Copyright © 1997-2002 [NEC Research Institute](#)